Behavioural types for non-uniform memory accesses

Juliana Franco and Sophia Drossopoulou

Imperial College London, United Kingdom

{j.vicente-franco, s.drossopoulou} @ imperial.ac.uk

Concurrent programs executing on NUMA architectures consist of concurrent entities (e.g. threads, actors) and data placed on different nodes. Execution of these concurrent entities often reads or updates states from remote nodes. The performance of such systems depends on the extent to which the concurrent entities can be executing in parallel, and on the amount of the remote reads and writes.

We consider an actor-based object oriented language, and propose a type system which expresses the topology of the program (the placement of the actors and data on the nodes), and an effect system which characterises remote reads and writes (in terms of which node reads/writes from which other nodes). We use a variant of ownership types for the topology, and a combination of behavioural and ownership types for the effect system.

1 Introduction

A prevalent paradigm in high performance machines is NUMA (non uniform memory access) systems, e.g., the AMD Bulldozer server[1]. NUMA systems have many *nodes* which contain processors and memory; Figure 1 shows the common NUMA structure. The nodes are connected with the other nodes through a system bus that allows processes running on a specific node to access the memory of the other nodes.

Memory access is either local, i.e. accessing memory in the local node, or remote, i.e. accessing memory of remote nodes. Remote accesses require requests to the system bus, and are thus more expensive than local accesses. Moreover, different remote accesses do not necessarily have the same cost (the time to obtain/write data in memory). Therefore, to characterize the communication (read/write) costs of a concurrent program, we need to know its topology (the placement of the actors and data on the nodes), and a characterisation of the reads and writes across nodes.

Figure 1: NUMA system [13].

In this work we consider a concurrent language based on actors (or active objects) and objects [5], which we call $\mathcal{L}_{numa}$, a language where, for the sake of simplicity, mutually recursive (synchronous and asynchronous) method invocations with communication are assumed to be not allowed and all the active objects must be created in the main class.

We develop a variant of ownership types [6] to express the location of actors and of data. In particular, we propose two levels of abstraction: classes have ownership (location) parameters, the main program defines the abstract locations and creates objects in these abstract locations; and at runtime the abstract locations are mapped to nodes (cf. Appendix C). We also propose a combination of behavioural and
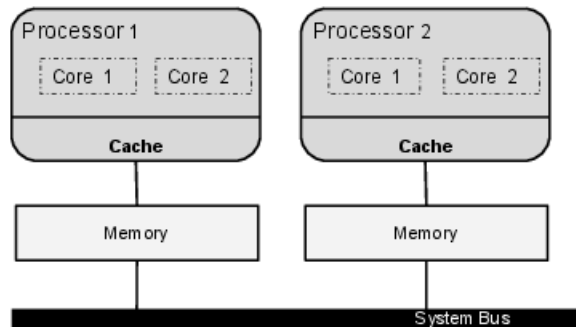
ownership types to characterise the interactions (reads, writes and messages sent) among objects located in different nodes.

Ownership types [4, 6, 12] were first introduced to statically describe the heap topology. Here we introduce ownership-like annotations to describe the system topology, that is, its nodes and where threads are running and data is allocated. Behavioural types [2, 8, 9, 14, 18] are usually used to describe and statically, or dynamically, verify patterns of interaction between processes/threads/participants of concurrent and parallel computations. Here we present a type system that allows the programmer to specify the interactions among objects located in different nodes, and therefore we abstract the communication made through the system bus.

Outline. This paper is organised as follows: Section 2 introduces the syntax of $\mathcal{L}_{numa}$, Section 3 gives the operational semantics, Section 4 presents the typing rules, and Section 4 shows properties of $\mathcal{L}_{numa}$, and finally Section 6 concludes. Several definitions are given in the appendix.

2   Syntax

Figure 2 presents the syntax of $\mathcal{L}_{numa}$. A program consists of a set of class declarations representing actors and passive objects. The use of the keyword active in a class declaration indicates that the class represents actors. Passive objects are similar to ordinary Java objects while actors have all the properties of passive objects, and in addition also have their own execution thread and may send messages to other actors. As in actor-based languages, messages are stored in private queues. A more detailed definition can be found in [5].

$$P \in Program ::= Cd^*$$
$$Cd \in ClassDecl ::= [\text{active}]\ \text{class}\ C\langle \overline{p}^+ \rangle\ \overline{Fd}\ \overline{Md}$$
$$Fd \in FieldDecl ::= f : T$$
$$Md \in MethodDecl ::= \text{def}\ m(x : T) :\ T\ \text{as}\ b\ \{e\}$$
$$ot \in OwnershipType ::= C\langle \overline{l}^+ \rangle$$
$$T \in Type ::= \text{bool} \mid \text{nil} \mid \boxed{\text{int}} \mid ot$$
$$l \in Location ::= p \mid L$$
$$val \in Value ::= \text{null} \mid \text{true} \mid \text{false}$$
$$var \in Variable ::= x \mid \text{this}$$

$$e \in Expr ::= var \mid val \mid \text{if}\ e\ \text{then}\ e\ \text{else}\ e$$
$$\mid e.m(e) \mid e!m(e) \mid e.f$$
$$\mid e.f = e \mid \text{new}\ ot$$
$$\mid \text{for}\ i\ \text{in}\ n_1..n_2\ \text{do}\ e$$
$$\mid \text{let}\ x = e\ \text{in}\ e \mid \boxed{\text{return}\ e}$$
$$\pi \in RemAccess ::= \text{rd}(l,l) \mid \text{wrt}(l,l) \mid \text{msg}(l,l,m)$$
$$bop ::= \pi \mid \{b\ \text{or}\ b\} \mid \text{Loop}(n : b)$$
$$b \in Behaviour ::= \varepsilon \mid bop.b \mid [b,b]$$

Figure 2: Syntax of classes and (behavioural) types. The boxed constructs are not user syntax. In the class declaration *Cd* the use of [ ] means that the keyword active is optional.

Each class, active or passive, is annotated with a set of location parameters $p_1, \ldots, p_n$ where p_1 represents the place where the instance of the class is allocated and $p_2, \ldots, p_n$ locations that can be used in the types of the rest of the class. The location parameters of the main class, $L_1, \ldots, L_n$, are abstractions of the concrete nodes, and at runtime will be mapped to concrete node identifiers.

A class declaration might have field and method declarations. A field declaration consists of a field identifier and a type; a method declaration consists of a method identifier, one parameter (variable and type), return type, behavioural type and an expression (method body). $\mathcal{L}_{numa}$ has the types bool, nil, and an ownership type $C\langle l_1, \ldots, l_n \rangle$ which represents objects located in l_1 that may contain references to

objects in locations $l_2, ..., l_n$. The syntax of expressions is similar to other OO programming languages; note only the asynchronous method call (message sending), $e!m(e)$.

The most interesting part of the syntax is our treatment of behavioural types. We have basic operations, π, which are reading from a remote node ($\mathsf{rd}(l,l)$), writing to a remote node ($\mathsf{wrt}(l,l)$), and message sending ($\mathsf{msg}(l,l,m)$)—this has to be reflected in the behaviour, as it adds messages to queues in remote memory. For all of them the first location is where the expression is being executed and the second is the location where a read/write is made or a message sent. We also have types to describe conditional expressions, $\{b \text{ or } b\}$, (the two branches in the if-then-else expression imply two branches in the type), and for-loops, $\mathsf{Loop}(n\colon b)$. A behavioural type, b, may be empty, ε, meaning that there is no "communication" across different nodes, the sequence of operations, $bop.b$, and two types in parallel, $[b,b]$, introduced by message sendings.

3   Semantics

We now describe the dynamic semantics of $\mathcal{L}_{numa}$. Nodes, $\mathcal{N}$, defined in Figure 3, aim to reflect NUMA nodes. Namely, a node in our formalism has an identifier, a heap with all the data allocated in it, and several execution threads *EThread*. An execution thread belongs to an actor, and has a stack and an expression being executed. A heap is a mapping from addresses to (passive and active) objects. An

$$
\begin{aligned}
\mathcal{N} &\in Node = NodeId \times Heap \times \overline{EThread} & o &\in Object = ClassId \times \overline{NodeId} \times \\
\mathcal{T} &\in EThread = Stack \times Expr & & (FieldId \rightarrow value) \times Queue \\
h &\in Heap = Addr \rightarrow Object & \alpha &\in Addr = NodeId \times \mathbb{N} \\
\sigma &\in Stack = Addr \times \overline{Frame} & v &\in value = val \mid Addr \mid \mathsf{skip} \mid \mathsf{NPE} \\
\varphi &\in Frame = var \rightarrow value & E[] &::= [\cdot] \mid [\cdot].m(e) \mid \alpha.m([\cdot]) \mid [\cdot]!m(e) \\
Q &\in Queue ::= \bullet \mid \emptyset \mid m(v) :: Q & & \mid \alpha!m([\cdot]) \mid [\cdot].f \mid [\cdot].f = e \\
\mathcal{L} &\in LocsMap = LocId \rightarrow NodeId & & \mid \alpha.f = [\cdot] \mid \mathsf{let}\ x = [\cdot]\ \mathsf{in}\ e \\
\kappa &\in NodeId = \mathbb{N} & & \mid \mathsf{if}\ [\cdot]\ \mathsf{then}\ e_1\ \mathsf{else}\ e_2 \mid \mathsf{let}\ x = [\cdot]\ \mathsf{in}\ e \\
l &\in Location ::=\ \text{as before} \mid \kappa & & \mid \mathsf{return}\ [\cdot]
\end{aligned}
$$

Figure 3: Dynamic Entities. We assume the existence of a map $\mathcal{L}$ that maps abstract locations (declared by the programmer in the main class) to NUMA node identifiers.

object consists of a class identifier C, a sequence of node identifiers representing the actual location parameters, a mapping from field identifiers to their values, and a message queue, where the queue of a passive object is $\bullet$. An address, α, consists of a node identifier, $\kappa \in NodeId$, and an offset, $n \in \mathbb{N}$.

In our system, a configuration $\overline{\mathcal{N}}$ can be reduced to another configuration $\overline{\mathcal{N}}'$ either without any communication or implying a remote access from one of the nodes to another node. In the first case, the rule [GsExec1] should be applied, where only one node is involved in the reduction. In the second case the rule [GsExec2] should be used, where two nodes are involved in the reduction, as shown in Figure 4.

In the same way, expression reduction may result in accessing remote memory or not; therefore we divide the operational semantics rules as follows:

1. Expressions that do not access memory or send messages. These are defined in Figure 5.

$$[\text{GsExec1}]$$

$$\frac{\kappa, h, \sigma, e \xrightarrow{\pi} h', \sigma', e'}{\overline{\mathcal{N}}, (\kappa, h, \overline{\mathcal{T}}, \langle \sigma, e \rangle) \xrightarrow{\pi} \overline{\mathcal{N}}, (\kappa, h', \overline{\mathcal{T}}, \langle \sigma', e' \rangle)}$$

$$[\text{GsExec2}]$$

$$\frac{\kappa_1, h_1, \sigma_1, e_1 \parallel \kappa_2, h_2 \xrightarrow{\pi} h'_1, \sigma'_1, e'_1 \parallel h'_2}{\overline{\mathcal{N}}, (\kappa_1, h_1, \overline{\mathcal{T}}_1, \langle \sigma_1, e_1 \rangle), (\kappa_2, h_2, \overline{\mathcal{T}}_2) \xrightarrow{\pi} \overline{\mathcal{N}}, (\kappa_1, h'_1, \overline{\mathcal{T}}_1, \langle \sigma'_1, e'_1 \rangle), (\kappa_2, h'_2, \overline{\mathcal{T}}_2)}$$

Figure 4: Global semantics

2. Expressions that result in accesses to memory. These are defined in Figure 6 and are further divided in:

   (a) The access happens locally—only one node required.

   (b) The access happens remotely—two different nodes required.

Figure 5 shows shows the rules for the point 1, where no accesses to memory, eiher in the same node or not, are made. Each rule takes a node identifier, its heap, a stack and an expression, and reduces to

$$[\text{SIfTrue}] \qquad\qquad\qquad\qquad\qquad [\text{SIfFalse}]$$

$$\frac{}{\kappa, h, \sigma, \text{if true then } e_1 \text{ else } e_2 \xrightarrow{\varepsilon} h, \sigma, e_1} \qquad \frac{}{\kappa, h, \sigma, \text{if false then } e_1 \text{ else } e_2 \xrightarrow{\varepsilon} h, \sigma, e_2}$$

$$[\text{SLet}] \qquad\qquad\qquad [\text{SRet}] \qquad\qquad\qquad [\text{SVar}]$$

$$\frac{x \text{ fresh in } \varphi \quad \varphi' = \varphi[x \mapsto v]}{\kappa, h, \sigma.\varphi, \text{let } x = v \text{ in } e \xrightarrow{\varepsilon} h, \sigma.\varphi', e} \qquad \frac{}{\kappa, h, \sigma.\varphi, \text{return } v \xrightarrow{\varepsilon} h, \sigma, v} \qquad \frac{\varphi(x) = v}{\kappa, h, \sigma.\varphi, x \xrightarrow{\varepsilon} h, \sigma.\varphi, v}$$

$$[\text{SFor}] \qquad\qquad\qquad\qquad\qquad [\text{SForSkip}]$$

$$\frac{x \text{ fresh in } \varphi \quad e' = (\text{let } x = e \text{ in for } i \text{ in } (n_1 + 1)..n_2 \text{ do } e)}{\kappa, h, \sigma.\varphi, \text{for } i \text{ in } n_1..n_2 \text{ do } e \xrightarrow{\varepsilon} h, \sigma.\varphi[i \mapsto n_1], e'} \qquad \frac{n_1 > n_2}{\kappa, h, \sigma, \text{for } i \text{ in } n_1..n_2 \text{ do } e \xrightarrow{\varepsilon} h, \sigma, \text{skip}}$$

$$[\text{SSkip}] \qquad\qquad\qquad\qquad\qquad\qquad [\text{SCallL}]$$

$$\frac{}{\kappa, h, \sigma, \text{skip} \xrightarrow{\varepsilon} h, \sigma, \text{null}} \qquad \frac{\text{owners}(h, \alpha) = C\langle \overline{\kappa} \rangle \quad \varphi = \alpha \cdot (\text{this} \mapsto \alpha, x \mapsto v)}{\kappa, h, \sigma, \alpha.m(v) \xrightarrow{\varepsilon} h, \sigma.\varphi, \text{return } \mathcal{M}(C, m) \downarrow_3 [\overline{\kappa}]}$$

$$[\text{SReceiveL}]$$

$$\frac{\alpha \downarrow_1 = \kappa \quad h(\alpha) = (C, \overline{\kappa}, \_, m(v) :: Q) \quad e = \mathcal{M}(C, m)[\overline{\kappa}]}{\kappa, h, \alpha \cdot \emptyset, \text{null} \xrightarrow{\varepsilon} h[\alpha \mapsto Q], \alpha \cdot (\text{this} \mapsto \alpha, x \mapsto v), \text{return } e}$$

$$[\text{SContextNPE}] \qquad\qquad\qquad\qquad [\text{SNPE}]$$

$$\frac{}{\kappa, h, \sigma, E[\text{NPE}] \xrightarrow{\varepsilon} h, \sigma, \text{NPE}} \qquad \frac{}{\kappa, h, \sigma, e_{npe} \xrightarrow{\varepsilon} h, \sigma, \text{NPE}}$$

where e_{npe} can be null.f, null.$f = e$, null.$m(e)$, null!$m(e)$, null$[i]$, null$[i] = e'$

Figure 5: Semantic rules for expressions that do not perform remote operations. Null-pointer exceptions included.

a new heap, a new stack and a new expression. They have the form $\kappa, h, \sigma, e \xrightarrow{\pi} h', \sigma', e'$. These rules show reduction without any communication among different nodes (they show reduction through ε). The intuition behind them is standart and similar can be found in the literature. Note only the rule for the message receiving, [SReceiveL], which takes an empty stack and a null expression, meaning that the expression of the thread being executed is fully reduced, and returns a new frame and expression after taking the next message in the queue to be processed. The expression returned is the body of the method asynchronously invoked, as there is a new frame with the values passed as arguments.

Figure 6 shows the semantic rules for the point 2. The rules on the left belong to 2(a); they have the same form of the rules introduced in Figure 5. The rules on the right belong to 2(b); they take two node identifiers, their heaps, a stack and an expression, and reduce to two new heaps, a new stack and a new expression. They have the form $\kappa_1, h_1, \sigma, e \parallel \kappa_2, h_2 \xrightarrow{\pi} h'_1, \sigma', e' \parallel h'_2$. In both cases they reduce through an operation described by π—the remote operation made or empty, ε (in the case of the absence of a remote operation). For instance, message sending in rule [SMsGL] adds a message to the queue of an

$$
\begin{array}{cc}
[\text{SMsGL}] & [\text{SMsGR}] \\
\dfrac{h' = h[\langle \kappa.n \rangle :: m(v)]}{\kappa, h, \sigma, \langle \kappa.n \rangle ! m(v) \xrightarrow{\varepsilon} h', \sigma, \text{null}} & \dfrac{\pi = \mathsf{msg}(\kappa_1, \kappa_2, m) \quad h'_2 = h_2[\langle \kappa_2.n \rangle :: m(v)]}{\kappa_1, h_1, \sigma, \langle \kappa_2.n \rangle ! m(v) \parallel \kappa_2, h_2 \xrightarrow{\pi} h_1, \sigma, \text{null} \parallel h'_2}
\end{array}
$$

$$
\begin{array}{cc}
[\text{SFReadL}] & [\text{SFReadR}] \\
\dfrac{}{\kappa, h, \sigma, \langle \kappa.n \rangle.f \xrightarrow{\varepsilon} h, \sigma, h(\kappa.n)(f)} & \dfrac{\pi = \mathsf{rd}(\kappa_1, \kappa_2) \quad v = h_2(\langle \kappa_2.n \rangle)(f)}{\kappa_1, h_1, \sigma, \langle \kappa_2.n \rangle.f \parallel \kappa_2, h_2 \xrightarrow{\pi} h_1, \sigma, v \parallel h_2}
\end{array}
$$

$$
\begin{array}{cc}
[\text{SFWriteL}] & [\text{SFWriteR}] \\
\dfrac{}{\kappa, h, \sigma, \langle \kappa.n \rangle.f = v \xrightarrow{\varepsilon} h[\langle \kappa.n \rangle, f \mapsto v], \sigma, v} & \dfrac{\pi = \mathsf{wrt}(\kappa_1, \kappa_2) \quad h'_2 = h_2[\langle \kappa_2.n \rangle, f \mapsto v]}{\kappa_1, h_1, \sigma, \langle \kappa_2.n \rangle.f = v \parallel \kappa_2, h_2 \xrightarrow{\pi} h_1, \sigma, v \parallel h'_2}
\end{array}
$$

$$
\begin{array}{cc}
[\text{SNewL}] & [\text{SNewR}] \\
\dfrac{\begin{array}{c}\kappa = \mathcal{L}(L_1) \quad \langle \kappa.n \rangle \notin \mathrm{dom}(h) \\ h' = h[\langle \kappa.n \rangle \mapsto \mathsf{initObj}(C\langle \overline{L} \rangle)]\end{array}}{\kappa, h, \sigma, \mathsf{new}\, C\langle \overline{L} \rangle \xrightarrow{\varepsilon} h', \sigma, \langle \kappa.n \rangle} & \dfrac{\begin{array}{c}\kappa_2 = \mathcal{L}(L_1) \quad \langle \kappa_2.n \rangle \notin \mathrm{dom}(h_2) \quad \pi = \mathsf{wrt}(\kappa_1, \kappa_2) \\ h'_2 = h_2[\langle \kappa_2.n \rangle \mapsto \mathsf{initObj}(C\langle \overline{L} \rangle)]\end{array}}{\kappa_1, h_1, \sigma, \mathsf{new}\, C\langle \overline{L} \rangle \parallel \kappa_2, h_2 \xrightarrow{\pi} h_1, \sigma, \langle \kappa_2.n \rangle \parallel h'_2}
\end{array}
$$

$$
\begin{array}{cc}
[\text{SContextL}] & [\text{SContextR}] \\
\dfrac{\kappa, h, \sigma, e \xrightarrow{\pi} h', \sigma', e'}{\kappa, h, \sigma, E[e] \xrightarrow{\pi} h', \sigma', E[e']} & \dfrac{\kappa_1, h_1, \sigma, e \parallel \kappa_2, h_2 \xrightarrow{\pi} h'_1, \sigma', e' \parallel h'_2}{\kappa_1, h_1, \sigma, E[e] \parallel \kappa_2, h_2 \xrightarrow{\pi} h'_1, \sigma', E[e'] \parallel h'_2}
\end{array}
$$

Figure 6: Set of semantic rules described in 2. The left rules show the reduction of expressions that execute locally (a) and the right rules, expressions that interact with remote objects (b).

actor in the same node as this, while [SMsGR] adds the message to the queue of an object in a different node. In the first case π is empty and in the second case it is $\mathsf{msg}(\kappa_1, \kappa_2, m)$. In both cases, the stack remains unchanged and the returned expression is null; namely execution is asynchronous. All the other rules, except the context rules, on the left show, as expected, reads and writes to the local heap and on the right present reads and writes to a remote heap.

4 Type Checking

Figure 7 shows the typing rules of $\mathcal{L}_{numa}$. They have the form $\overline{\Gamma} \vdash e \triangleright T, b$ where an expression e is verified against a sequence of typing contexts $\overline{\Gamma}$ resulting in a type T and an effect b. A typing context is a mapping from variables and addresses to types:

$$\Gamma \in TypingContext = (var \cup Addr) \to Type$$

The effect b describes the behaviour of e, that is, the memory accesses and messages sent to remote locations. Effects are concatenated via the function $\circ$ as defined below.

$$\varepsilon \circ b = b \qquad (bop.b_1) \circ b_2 = bop.(b_1 \circ b_2) \qquad [b_1, b_2] \circ b_3 = [b_1 \circ b_3, b_2]$$

The type T associated to an expression is found in a standard way: similar can be found in [4],

<div>

[T-VAR/ADDR]

$$\frac{}{\overline{\Gamma}.\Gamma \vdash var \triangleright \Gamma(var), \varepsilon}$$
$$\overline{\Gamma}.\Gamma \vdash \alpha \triangleright \Gamma(\alpha), \varepsilon$$

[T-TRUE/FALSE]

$$\frac{}{\overline{\Gamma} \vdash \mathsf{true} \triangleright \mathsf{bool}, \varepsilon}$$
$$\overline{\Gamma} \vdash \mathsf{false} \triangleright \mathsf{bool}, \varepsilon$$

[T-SKIP/NULL]

$$\frac{}{\overline{\Gamma} \vdash \mathsf{skip} \triangleright \mathsf{nil}, \varepsilon}$$
$$\overline{\Gamma} \vdash \mathsf{null} \triangleright \mathsf{nil}, \varepsilon$$

[T-LET]

$$\frac{\overline{\Gamma}.\Gamma \vdash e_1 \triangleright T_1, b_1 \quad x \notin \mathsf{dom}(\Gamma) \quad \overline{\Gamma}.\Gamma[x \mapsto T_1] \vdash e_2 \triangleright T_2, b_2}{\overline{\Gamma}.\Gamma \vdash \mathsf{let}\ x = e_1\ \mathsf{in}\ e_2 \triangleright T_2, b_1 \circ b_2}$$

[T-COND]

$$\frac{\overline{\Gamma} \vdash e_1 \triangleright \mathsf{bool}, b_1 \quad \overline{\Gamma} \vdash e_2 \triangleright T, b_2 \quad \overline{\Gamma} \vdash e_3 \triangleright T, b_3}{\overline{\Gamma} \vdash \mathsf{if}\ e_1\ \mathsf{then}\ e_2\ \mathsf{else}\ e_3 \triangleright T, b_1 \circ \{b_2\ \mathsf{or}\ b_3\}}$$

[T-FOR]

$$\frac{k > j \quad \overline{\Gamma} = \overline{\Gamma}'.\Gamma \quad \overline{\Gamma}'.\Gamma[i \mapsto \mathsf{int}] \vdash e \triangleright T, b}{\overline{\Gamma} \vdash \mathsf{for}\ i\ \mathsf{in}\ j..k\ \mathsf{do}\ e \triangleright T, \mathsf{Loop}(k-j+1 : b)}$$

[T-RET]

$$\frac{\overline{\Gamma} \vdash e \triangleright T, b}{\overline{\Gamma}.\Gamma \vdash \mathsf{return}\ e \triangleright T, b}$$

[T-NEWO]

$$\frac{\mathsf{isActive}(C) \implies \mathsf{isMain}(\overline{\Gamma}, \mathsf{this}) \quad ot = C\langle l_1, ..., l_n \rangle \quad l_1 \neq ... \neq l_n}{\overline{\Gamma} \vdash \mathsf{new}\ ot \triangleright ot, \mathsf{wrt}(\ell(\overline{\Gamma}), l_1)}$$

[T-FWRITE]

$$\frac{\overline{\Gamma} \vdash e \triangleright C\langle \bar{l} \rangle, b_1 \quad \mathcal{F}(C, f)[\bar{l}] = T \quad \overline{\Gamma} \vdash e' \triangleright T, b_2}{\overline{\Gamma} \vdash e.f = e' \triangleright T, b_1 \circ b_2 \circ \mathsf{wrt}(\ell(\overline{\Gamma}), l_1)}$$

[T-FREAD]

$$\frac{\overline{\Gamma} \vdash e \triangleright C\langle \bar{l} \rangle, b_1 \quad \mathcal{F}(C, f)[\bar{l}] = T}{\overline{\Gamma} \vdash e.f \triangleright T, b_1 \circ \mathsf{rd}(\ell(\overline{\Gamma}), l_1)}$$

[T-CALL]

$$\frac{\overline{\Gamma} \vdash e_1 \triangleright C\langle \bar{l} \rangle, b_1 \quad \overline{\Gamma} \vdash e_2 \triangleright T', b_2 \quad \ell(\overline{\Gamma}) = l_1 \quad \mathcal{M}(C, m)[\bar{l}] = (T, T', e_3, b_3)}{\overline{\Gamma} \vdash e_1.m(e_2) \triangleright T, b_1 \circ b_2 \circ b_3}$$

[T-MESSAGE]

$$\frac{\overline{\Gamma} \vdash e_1 \triangleright C\langle \bar{l} \rangle, b_1 \quad \overline{\Gamma} \vdash e_2 \triangleright T', b_2 \quad \ell(\overline{\Gamma}) = l_0 \quad \mathcal{M}(C, m)[\bar{l}] = (\mathsf{nil}, T', e_3, b)}{\overline{\Gamma} \vdash e_1!m(e_2) \triangleright \mathsf{nil}, b_1 \circ b_2 \circ \mathsf{msg}(l_0, l_1, m).[\varepsilon, b]}$$

</div>

Figure 7: Typing rules

therefore we focus only in the behaviour produced. The rules for variables and values, [T-VAR/ADDR], [T-TRUE/FALSE], [T-SKIP/NULL] result in empty effects, ε, because they do not represent any communication. The typing rule [T-LET] results in the concatenation of the behaviour of both expressions. The resulting behaviour of the rule [T-COND] is the behaviour of the predicate concatenated with a choice type which describes the behaviour of both branches. The rule [T-FOR] returns a loop type $\mathsf{Loop}(n : b)$, where n is the number of iterations of the loop and b is the behavioural type of its body.

The behaviour of the creation of an object, with [T-NEWO], is a write behaviour, from the location of this to the location of the new object, as new data is written to memory. The predicate $\mathsf{isActive}(C)$ is true if the class of the object being created in annotated as active and the predicate $\mathsf{isMain}(\overline{\Gamma}, \mathsf{this})$ is true if the class being verified is the main class. The field write is also represented by the write behaviour,

given that it changes data already in memory. Typing the expression $e.f = e'$ with the rule [T-FWRITE] returns the concatenation of the behaviour of e, the behaviour of e' and the write from the location of this to the location of the object changed. Following the same idea, the field read, $e.f$, is represented by the read behaviour and therefore the rule [T-FREAD] gives the concatenation of the behaviour of e with a read type from the location of this and to the location of the object read. The typing rule, [T-CALL], describes synchronous method invocation which is only allowed if the receiver is in the same location as the this object. Its behaviour is the behaviour of the receiver concatenated with the behaviour of the expression passed as argument and the behavioural type annotated in the body of the invoked method. The typing rule for the message send, [T-MESSAGE], is similar. However, it is possible to send a message to a different location and moreover it introduces parallelism in our types: the receiving of the message should be executed in parallel with the continuation of the message sending—the resulting behaviour has the continuation type, which in this case is ε, in parallel with the expression to be executed due the message received.

5   The global behaviour

We define a global behaviour, Σ, as a sequence of behavioural types

$$\Sigma \in \overline{Behaviour}$$

The behaviour of a node $\mathbb{N}$ describes the remote reads, writes and message sends to be executed by the node; it is obtained from the behaviour of the execution threads and message queues of all actors in that node. The global behaviour of a runtime configuration, $\overline{\mathbb{N}}$, describes the remote reads, writes and message sends to be executed by all nodes; it is the parallel combination of the behaviours of each the nodes $\mathbb{N}_i$. Both definitions, the behaviour of a node and the global behaviour of a configuration, are below.

Definition 1 (The global behaviour).

(1) $\mathbb{N}_1, \ldots, \mathbb{N}_n \blacktriangleright \overline{b}_1, \ldots, \overline{b}_n$ *iff* $\forall i \in 1..n : \mathbb{N}_i \blacktriangleright \overline{b}_i$

(2) $\kappa, h, \langle \sigma_1, e_1 \rangle, \ldots, \langle \sigma_n, e_n \rangle \blacktriangleright b_1, \ldots, b_n$ *iff* $\forall i \in 1..n : h, \sigma_i, e_i \blacktriangleright b_i$

(3) $h, \sigma, e \blacktriangleright \text{filter}(b \circ b_1 \circ \ldots \circ b_n)$ *iff* $\exists T : h, \sigma \vdash e \triangleright T, b \ \wedge \ (h(\sigma\downarrow_1) = (C, \kappa^+, \_, m_1(v_1) :: \ldots :: m_n(v_n) :: \emptyset)$
$\wedge \ \forall j \in 1..n : \exists T : h, (\text{this} \mapsto \sigma\downarrow_1, x \mapsto v_j) \vdash \mathcal{M}(C, m_j)[\kappa^+] \triangleright T, b_j)$

Using this notion of global behaviour, we implicitly assume a well-formed program and we state soundness of our typing, which says that if a well-formed configuration, $\overline{\mathbb{N}}$, with a global behaviour Σ, reduces to another configuration $\overline{\mathbb{N}}'$ through a communication step π then the resulting configuration $\overline{\mathbb{N}}'$ will have behaviour Σ' which is a reduction of Σ through π.

Theorem 1. *If* $\vdash \overline{\mathbb{N}} \ \wedge \ \overline{\mathbb{N}} \blacktriangleright \Sigma \ \wedge \ \overline{\mathbb{N}} \xrightarrow{\pi} \overline{\mathbb{N}}'$ *then* $\exists \Sigma' : \overline{\mathbb{N}}' \blacktriangleright \Sigma' \ \wedge \ \Sigma \sqsubseteq_\pi \Sigma'$

The definitions of well-formed configuration (including well-formed heap and well-formed stack) and (global) behaviour reduction are defined below:

Definition 2 (Well-formed (1) configuration, (2) node, (3) heap, (4) stack and (5) stack frame)**.**

    (1) $\vdash \overline{\mathcal{N}}$ ***iff*** $\forall i,j : \mathcal{N}_i{\downarrow}_1 = \mathcal{N}_j{\downarrow}_1 \implies i = j \ \wedge \ \forall \mathcal{N}' : \overline{\mathcal{N}} \vdash \mathcal{N}'$

    (2) $\overline{\mathcal{N}} \vdash \kappa, h, (\langle \sigma_1, e_1 \rangle, ..., \langle \sigma_n, e_n \rangle)$ ***iff***

        $\forall \alpha \in \mathrm{dom}(h) : \alpha{\downarrow}_1 = \kappa \ \wedge \ h(\alpha){\downarrow}_2 = \kappa, \_ \ \wedge \ \overline{\mathcal{N}} \vdash h$

        $\wedge \ \forall i \in \{1..n\} : \mathrm{heaps}(\overline{\mathcal{N}}) \vdash \sigma_i \ \wedge \ \exists T_i, b_i : h, \sigma_i \vdash e_i \triangleright T_i, b_i$

    (3) $\overline{\mathcal{N}} \vdash h$ ***iff*** $\forall \alpha \in \mathrm{dom}(h) : \mathrm{heaps}(\overline{\mathcal{N}}) \vdash \alpha : \mathrm{owners}(h, \alpha)$

    (4) $h \vdash \alpha \cdot \varphi_1, ..., \varphi_n$ ***iff*** $\forall i \in \{1..n\} : h \vdash \varphi_i$

    (5) $h \vdash (\mathsf{this} \mapsto \alpha, x_1 \mapsto v_1, \dots, x_n \mapsto v_n)$ ***iff*** $\{\alpha, v_1...v_n\} \subseteq \{\mathsf{true}, \mathsf{false}, \mathsf{null}\} \cup \mathrm{dom}(h)$

Definition 3 (Global behaviour reduction)**.**

$$\Sigma \sqsubseteq_\pi \Sigma' \textit{ iff } \Sigma = \overline{b}_1, b, \overline{b}_2 \ \wedge \ \Sigma' = \overline{b}'_1, b', \overline{b}'_2 \ \wedge \ b \sqsubseteq_\pi b' \ \wedge$$
$$(b = [b_1, b_2] \implies b' = b_1 \ \wedge \ \exists b_j \in \Sigma, b'_j \in \Sigma' : b'_j = b_j \circ b_2)$$

Definition 4 (Behaviour reduction)**.**

$$b_1 \sqsubseteq_\pi b_2 \textit{ iff } b_1 = \pi.b_2$$
$$b_1 \sqsubseteq_\varepsilon b_2 \textit{ iff } b_1 = b_2 \ \vee \ b_1 = \{b_2 \text{ or } \_\} \ \vee \ b_1 = \{\_ \text{ or } b_2\} \ \vee$$
$$(b_1 = \mathsf{Loop}(n : b).b' \ \wedge \ b_2 = b.\mathsf{Loop}(n-1 : b).b') \ \vee \ b_1 = [b_2, \_]$$

Theorem 1 is a corollary of Lemmas 1 and 2.

Lemma 1. *If* $\overline{\mathcal{N}} \vdash h \ \wedge \ h \vdash \sigma \ \wedge \ \kappa, h, \sigma, e \xrightarrow{\pi} h', \sigma', e' \ \wedge \ h, \sigma \vdash e \triangleright T, b \ \wedge \ \neg(\sigma{\downarrow}_2 = \emptyset \ \wedge \ e = \mathsf{null})$ *then* $\exists b' : h', \sigma' \vdash e' \triangleright T, b' \ \wedge \ \mathrm{filter}(b) \sqsubseteq_\pi \mathrm{filter}(b')$

Lemma 2. *If* $\overline{\mathcal{N}} \vdash h_1 \ \wedge \ \overline{\mathcal{N}} \vdash h_2 \ \wedge \ h_1 \cup h_2 \vdash \sigma \ \wedge \ \kappa_1, h_1, \sigma, e \parallel \kappa_2, h_2 \xrightarrow{\pi} h'_1, \sigma', e' \parallel h'_2 \ \wedge \ h_1 \cup h_2, \sigma \vdash e \triangleright T, b$ *then* $\exists b' : h'_1 \cup h'_2, \sigma' \vdash e' \triangleright T, b' \ \wedge \ \mathrm{filter}(b) \sqsubseteq_\pi \mathrm{filter}(b')$

6 Final Remarks

Related Work. To the best of our knowledge there is no integration of behavioural types in the active/passive object paradigm, or any formalism that combines behavioural types with ownership types to describe memory accesses; however there are already a few programming languages that use session (behavioural) types in actor-based languages, namely: the integration of session types in a Featherweight Erlang introduced by Mostrous and Vasconcelos [10]; an implementation of multiparty session types in an actor library written in Python presented by Neykova and Yoshida [11]; and the behavioural type system for an actor calulus, proposed by Crafa [7].

    With respect to programming languages with the notion of locations and proximity among processes and data, Rinard presented an extension of the programming language Jade (an implicitly parallel programming language designed to explore task-level concurrency [16]) that allows the execution of tasks close to the data that they will use [15]. The language has constructs to describe how the processes access to the data; this information is analysed and used to improve the communication. Given that it is more expensive to access data remotely than locally, the author introduces a locality optimization algorithm that schedules the execution of tasks on places (processors) close to the data. The programming language X10 [17], developed by IBM, also features a notion of locality/places. In X10, each object can be either

assigned to a place or distributed among different places. Chandra et al. [3] presented a new dependent type system for X10 that captures information about the locality of the resources in a partitioned heap for distributed data structures, called *place types*. It provides information not only about whether a reference is local or remote, but also if two remote references point to resources in the same place or not. Therefore, the compiler may use this information to decrease the runtime overhead.

Conclusion. This paper presents the fomalisation of a small oject-oriented programming language that amalgamates behavioural types with ownership types in order to describe remote memory accesses in NUMA systems. Ownership types play a role in the representation of the topology and behavioural types in the definition of reads, writes and messages sent to remote locations. This sequence of memory access operations are annotated in the method declarations as the ownership/location parameters are annotated in class declarations. This formalisation is just the first step towards a programming language that optimises performance by moving objects to nodes where they have a cheaper cost (the cost of interacting with other objects and of doing remote accesses).

References

[1] *AMD Bulldozer Server*. `http://www.amd.com/en-us/products/server`.

[2] Giuseppe Castagna & Luca Padovani (2009): *Contracts for Mobile Processes*. In: *CONCUR'2009*, LNCS, Springer, pp. 211–228, doi:10.1007/978-3-642-04081-8_15.

[3] Satish Chandra, Vijay Saraswat, Vivek Sarkar & Rastislav Bodik (2008): *Type Inference for Locality Analysis of Distributed Data Structures*. In: *PPoPP '08*, ACM, pp. 11–22, doi:10.1145/1345206.1345211.

[4] Dave Clarke & Sophia Drossopoulou (2002): *Ownership, Encapsulation and the Disjointness of Type and Effect*. SIGPLAN Not. 37, pp. 292–310, doi:10.1145/583854.582447.

[5] Dave Clarke, Tobias Wrigstad, Johan Östlund & Einar Broch Johnsen (2008): *Minimal Ownership for Active Objects*. In: *APLAS '08*, Springer, pp. 139–154, doi:10.1007/978-3-540-89330-1_11.

[6] David G. Clarke, John M. Potter & James Noble (1998): *Ownership Types for Flexible Alias Protection*. In: *OOPSLA '98*, ACM, pp. 48–64, doi:10.1145/286936.286947.

[7] Silvia Crafa (2012): *Behavioural Types for Actor Systems*. Technical Report. Available at `http://arxiv.org/abs/1206.1687`.

[8] Kohei Honda, Vasco T. Vasconcelos & Makoto Kubo (1998): *Language primitives and type disciplines for structured communication-based programming*. In: *European Symposym on Programming*, LNCS 1381, Springer, pp. 22–138, doi:10.1007/BFb0053567.

[9] Naoki Kobayashi (2003): *Type Systems for Concurrent Programs*. In: *Formal Methods at the Crossroads. From Panacea to Foundational Support*, LNCS 2757, Springer, pp. 439–453, doi:10.1007/978-3-540-40007-3_26.

[10] Dimitris Mostrous & Vasco Thudichum Vasconcelos (2011): *Session Typing for a Featherweight Erlang*. In: *COORDINATION 2011*, LNCS 6721, Springer, pp. 95–109, doi:10.1007/978-3-642-21464-6_7.

[11] Rumyana Neykova & Nobuko Yoshida (2014): *Multiparty Session Actors*. In: *COORDINATION 2014*, LNCS 8459, Springer, pp. 131–146, doi:10.1007/978-3-662-43376-8_9.

[12] James Noble, Jan Vitek & John Potter (1998): *Flexible Alias Protection*. In: *ECOOP'98*, Springer, pp. 158–185, doi:10.1007/BFb0054091.

[13] *Optimizing Applications for NUMA.* https://software.intel.com/en-us/articles/optimizing-applications-for-numa.

[14] B. Pierce & D. Sangiorgi (1993): *Typing and subtyping for mobile processes.* In: *LICS'93*, pp. 376–385, doi:10.1109/LICS.1993.287570.

[15] Martin C. Rinard (1997): *Locality Optimizations for Parallel Computing Using Data Access Information.* International Journal of High Speed Computing 9(2), pp. 161–179, doi:10.1142/S0129053397000118.

[16] Martin C. Rinard, Daniel J. Scales & Monica S. Lam (1993): *Jade: A High-Level, Machine-Independent Language for Parallel Programming.* IEEE Computer 26, doi:10.1109/2.214440.

[17] Vijay A. Saraswat, Vivek Sarkar & Christoph von Praun (2007): *X10: Concurrent Programming for Modern Architectures.* In: *PPoPP '07*, ACM, pp. 271–271, doi:10.1145/1229428.1229483.

[18] R.E. Strom & S. Yemini (1986): *Typestate: A programming language concept for enhancing software reliability.* IEEE Transactions on Software Engineering, pp. 157–171, doi:10.1109/TSE.1986.6312929.

A Identifier Conventions and Semantics

Identifier conventions.

$$n \in \mathbb{N} \quad C \in \textit{ClassId} \quad m \in \textit{MethId} \quad f \in \textit{FieldId} \quad L \in \textit{LocId} \quad p \in \textit{OwnershipId} \quad x,i \in \textit{varId}$$

B Auxiliary definitions, shorthands and lookup functions

Definition 5 (Well-formed program and class).

$$\vdash P \equiv \forall([\text{active}] \text{ class } C\langle...\rangle... \in P) : P \vdash C \qquad P \vdash C \equiv \begin{cases} \mathcal{O}(C) = \{p_1,...,p_n\} \wedge \\ \forall m : \mathcal{M}(C,m) = (T,x:T',e,b) \wedge \\ (\text{this} \mapsto C\langle p_1,...,p_n\rangle, x \mapsto T') \vdash e \triangleright T, b' \\ \implies b = \text{filter}(b') \end{cases}$$

Given that the effects returned during type checking do not exclude reads and writes happening in the same node, we apply a function filter(b) in order to exclude such annotations. The function is define as follows.

$$\text{filter}(\varepsilon) = \varepsilon \qquad \text{filter}([b_1, b_2]) = [\text{filter}(b_1), \text{filter}(b_2)]$$

$$\text{filter}(\pi.b) = (\text{if source}(\pi) = \text{dest}(\pi) \text{ then } \varepsilon \text{ else } \pi).\text{filter}(b)$$

$$\text{filter}(\{b_1 \text{ or } b_2\}.b_3) = (\text{if filter}(b_1) = \varepsilon \wedge \text{filter}(b_2) = \varepsilon \text{ then } \varepsilon \text{ else } \{\text{filter}(b_1) \text{ or filter}(b_2)\}).\text{filter}(b_3)$$

$$\text{filter}(\text{Loop}(n: b).b') = (\text{if filter}(b) = \varepsilon \text{ then } \varepsilon \text{ else } \text{Loop}(n: \text{filter}(b))).\text{filter}(b')$$

Note that if the expressiosns nested in for-loops or conditional expressions have behaviour ε, then the the loop or choice types are not annotated.

Definition 6 (Value agreement).

$$\frac{}{h \vdash \text{true} : \text{bool}} \text{[WFTRUE]} \qquad \frac{}{h \vdash \text{false} : \text{bool}} \text{[WFFALSE]} \qquad \frac{T = \text{nil} \vee \text{isValid}(T)}{h \vdash \text{null} : T} \text{[WFNULL]}$$

$$\frac{h(\alpha) = (C,(\overline{\kappa}),(f_i \mapsto v_i)_{i \in I}, \bullet) \quad \forall i \in I : h \vdash v_i : \mathcal{F}(C,f_i)[\overline{\kappa}]}{h \vdash \alpha : C\langle\overline{\kappa}\rangle} \text{[WFOBJ]}$$

$$\frac{\textit{For } I \textit{ some index set} \quad h(\alpha) = (C,(\overline{\kappa}),(f_i \mapsto v_i)_{i \in I}, m_1(v_1) :: ... :: m_n(v_n) :: \emptyset)}{\forall i \in I : h \vdash v_i : \mathcal{F}(C,f_i)[\overline{\kappa}] \quad h, \alpha \cdot (\text{this} \mapsto \alpha, x \mapsto v_i) \vdash v_i \triangleright \mathcal{M}(C,m_i){\downarrow_2}\,[\overline{\kappa}], b}{h \vdash \alpha : C\langle\overline{\kappa}\rangle} \text{[WFAOBJ]}$$

Lookup functions Considering P, the globally accessible program, and the class declaration

$$\text{class } C\langle p^+ \rangle \{ \overline{Fd} \; \overline{Md} \} \in P$$

we define:

$$\mathcal{O}(C) = \{ p^+ \}$$
$$\mathcal{F}(C, f) = T \text{ iff } f : T \in \overline{Fd}$$
$$\mathcal{F}_s(C) = \{ \overline{Fd} \}$$
$$\mathcal{M}(C, m) = (T, T', e, b) \text{ iff def } m(x : T') : T \text{ in } b \{ e \} \in \overline{Md}$$
$$\mathcal{F}(C, f)[l_1, ..., l_n] = \mathcal{F}(C, f)[l_1/p_1, ..., l_n/p_n] \text{ where } \mathcal{O}(C) = \{ p_1, ..., p_n \}$$

Operations on the heap

$$h[\alpha \mapsto o] = h' \text{ where } h'(\alpha) = o \; \wedge \; \forall \alpha_i \in \text{dom}(h) \setminus \{ \alpha \} : \; h(\alpha_i) = h'(\alpha_i)$$
$$h[\alpha, f \mapsto v] = h' \text{ where } h'(\alpha) = h(\alpha)[f \mapsto v] \; \wedge \; \forall \alpha_i \in \text{dom}(h) \setminus \{ \alpha \} : \; h(\alpha_i) = h'(\alpha_i)$$
$$h[\alpha :: m(v)] = h' \text{ where } h(\alpha) = o \; \wedge \; o{\downarrow}_4 \neq \bullet \; \wedge \; h' = h[\alpha \mapsto (o{\downarrow}_1, o{\downarrow}_2, o{\downarrow}_3, m(v) :: o{\downarrow}_4)]$$
$$\wedge \; \forall \alpha_i \in \text{dom}(h) \setminus \{ \alpha \} : \; h(\alpha_i) = h'(\alpha_i)$$
$$\text{owners}(h, \alpha) = C\langle \overline{\kappa} \rangle \text{ where } h(\alpha){\downarrow}_1 = C \; \wedge \; h(\alpha){\downarrow}_2 = \overline{\kappa}$$
$$h_1 \cup h_2 = h \text{ where } \forall \alpha \in \text{dom}(h) : h_1(\alpha) = h(\alpha) \; \vee \; h_2(\alpha) = h(\alpha)$$

Operations on objects

$$o(f) \equiv o{\downarrow}_3 (f)$$
$$o[f \mapsto v] \equiv (o{\downarrow}_1, o{\downarrow}_2, (f \mapsto v, \overline{f_i \mapsto v_i}), o{\downarrow}_4) \qquad \textbf{where } o{\downarrow}_3 = f \mapsto \_, \overline{f_i \mapsto v_i}$$
$$\text{initObj}(C\langle L_1, ..., L_m \rangle) \equiv \begin{cases} (C, \kappa_1, ..., \kappa_m, (f_i \mapsto \text{init}(T_i))_{i \in 1..n}, \emptyset) & \text{isActive}(C) \\ (C, \kappa_1, ..., \kappa_m, (f_i \mapsto \text{init}(T_i))_{i \in 1..n}, \bullet) & \text{otherwise} \end{cases}$$
$$\textbf{where } \mathcal{F}_s(C) = \{ f_1 : T_1, ..., f_n : T_n \} \text{ and } \forall j \in \{ 1..m \} : \kappa_j = \mathcal{L}(L_j)$$

Operations on types

$$\text{init}(T) \equiv \text{if } T = \text{bool then false else null} \qquad \ell(\overline{\Gamma}) = l \textbf{ iff } \overline{\Gamma} = \_.\Gamma \; \wedge \; \Gamma(\text{this}) = C\langle l, \_ \rangle$$

Other definitions

$$e[C, \kappa_1, ..., \kappa_n] = e[\kappa_1/p_1, ..., \kappa_n/p_n] \text{ where } \mathcal{O}(C) = \{ p_1, ..., p_n \}$$
$$\text{heaps}(\mathcal{N}_1, ..., \mathcal{N}_n) = h_1 \cup \cdots \cup h_n \textbf{ iff } \forall i \in \{ 1..n \} : \mathcal{N}_i{\downarrow}_2 = h_i$$
$$h, \sigma \vdash e \triangleright T, b \textbf{ iff } \text{buildContext}(h, \sigma) \vdash e \triangleright T, b$$
$$\text{typeOf}(h, v) \equiv \text{if } v = \text{true} \; \vee \; v = \text{false then bool else } \text{owners}(h, v)$$

$$\frac{\begin{array}{c} \text{buildContext}(h, \varphi_1) = \Gamma_n \\ \cdots \\ \text{buildContext}(h, \varphi_n) = \Gamma_1 \end{array}}{\text{buildContext}(h, \alpha \cdot \varphi_1 \ldots \varphi_n)} \qquad \frac{\begin{array}{c} T_{this} = \text{typeOf}(h, \alpha) \\ T_1 = \text{typeOf}(h, v_1) \quad \ldots \quad T_n = \text{typeOf}(h, v_n) \\ \Gamma = (\text{this} \mapsto T_{this}, x_1 \mapsto T_1, ..., x_n \mapsto T_n) \end{array}}{\text{buildContext}(h, \text{this} \mapsto \alpha, x_1 \mapsto v_1, ..., x_n \mapsto v_n) = \Gamma}$$

C   Topology Example

Consider the following code with three class declarations: an active class C, a passive D and the class Main. An active object, instance of C, has three fields pointing to three objects in different locations of type D. The class main creates three abstract (or symbolic) locations L1, L2, L3 and the body of the main method.

```
                                    class Main⟨L1, L2, L3⟩
active class C⟨p1, p2, p3⟩            def main(): nil
   d1: D⟨p1⟩                            as b write(L1, L2). write(L1, L3) {
   d2: D⟨p2⟩                            let x = new C ⟨L1, L2, L3⟩ in
   d3: D⟨p3⟩                            let y = (x.d1 = new D⟨L1⟩) in
                                        let z = (x.d2 = new D⟨L2⟩) in
class D⟨p⟩                                      x.d3 = new D⟨L3⟩
                                    }
```

The topology after execution of the main method is depicted in the following figure. In the abstract location
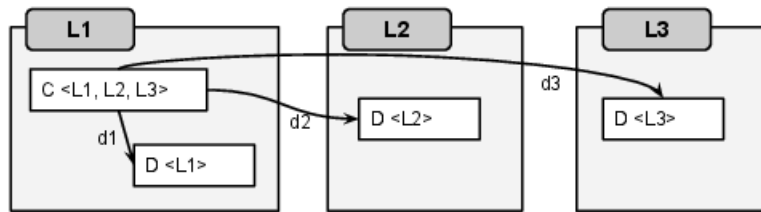

Figure 8: The ownership topology after the execution of the expression in the method main.

L1 there is an instance of class C and an instance of class D. Abstract locations L2 and L3 have both an instance of class D. Although the programmer define 3 abstract locations, the machine might have a different number of nodes. For instance, in a system with two different nodes, we could have the mapping $(L_1 \mapsto \kappa_1, L_2 \mapsto \kappa_2, L_3 \mapsto \kappa_2)$ between abstract locations and node identifiers, which means that the objects in L1 are in the node κ_1, and objects from L2 and L3 are in the same node, as depicted in Figure 9.
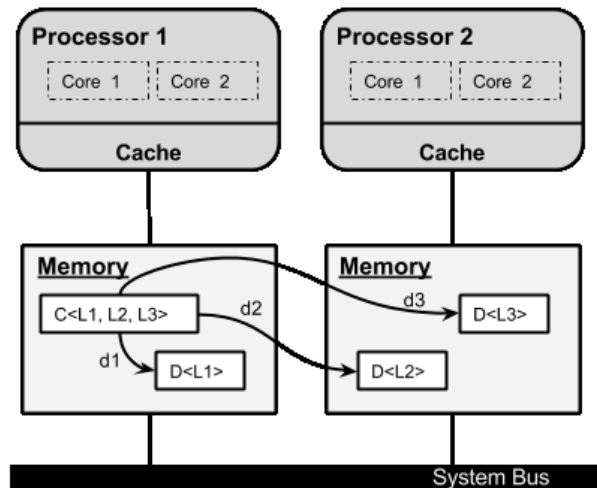

Figure 9: NUMA system with two different nodes